



ARTICo³ in the CERBERO H2020 EU Project

ARTICo³ is integrated in the CERBERO toolchain to provide adaptive hardware acceleration using Dynamic and Partial Reconfiguration in FPGA devices.

Open Source Framework

ARTICo³ is an open-source framework available on GitHub. Documentation and tutorials are also available online for you to use it in your own projects.

ARTICo³ is supported by an increasing community of developers in the field of reconfigurable embedded systems.



<https://github.com/des-cei/artico3>



MORE INFO AT

- <https://des-cei.github.io/tools/artico3>
- <https://github.com/des-cei/artico3>



ARTICo³ is an open-source run-time reconfigurable processing architecture to enable hardware-accelerated high-performance embedded computing. It is highly flexible, offering adaptive computing performance, energy efficiency, and fault tolerance on demand.



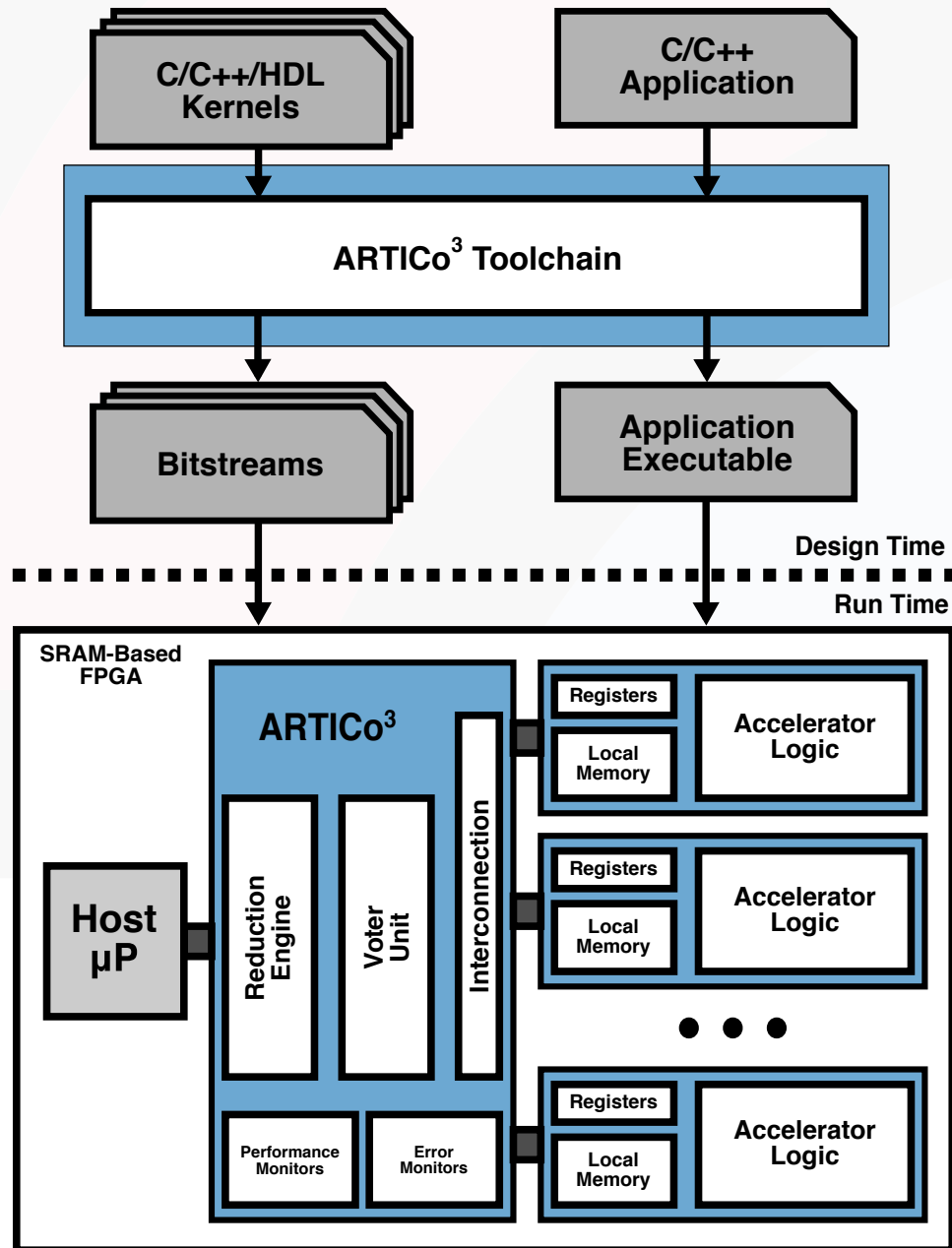
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Framework Components

The **ARTICo³ architecture** is complemented at **design time** with an **automated toolchain** to build reconfigurable multi-accelerator systems, and at **run time** with a **software library** to transparently manage both reconfiguration and parallel execution processes.

Main Features

Hardware Design made easy

Write your accelerators in C/C++ and exploit High-Level Synthesis.

FPGA Reconfiguration made easy

Forget about complex design flows and low-level technology limitations.

Parallel Processing made easy

Offload computations to the FPGA fabric transparently using a lightweight API.